



<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Applicant(s): <b>MARUTA, Yukio, et al.</b>	Serial No. <b>10/708,587</b>
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### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

### FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____	AE		
_____	AF		
_____	AG		
_____	AH		
_____	AI		

### OTHER DOCUMENTS

_____	AJ	"Achieving Quick Design Debugging With Circuit Correction Function Using Focused Ion Beam Technique"; IBM, June 23, 1999, <a href="http://www-6.ibm.com/jp/chips/v5_1/mn51011.html">http://www-6.ibm.com/jp/chips/v5_1/mn51011.html</a> .	
_____	AK		
Examiner	<i>R. Pignatelli</i>	Date Considered	<i>6/22/05</i>